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Attorney Docket No. 01296/LH

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): T. KAWANO

Serial No. : 09/840,193

Filed

: April 23, 2001

For

: RADIATION IMAGE PROCESSING

APPARATUS

Art Unit

Examiner :

## PRELIMINARY AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

SIR:

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date noted below.

Attorney: Leonard Holt.

Dated: August 21, 2001

In the event that this Paper is late filed, and the necessary petition for extension of time is not filed concurrently herewith, please consider this as a Petition for the requisite extension of time, and to the extent not tendered by check attached hereto, authorization to charge the extension fee, or any other fee required in connection with this Paper, to Account No. 06-1378.

Please amend the above-identified application, as follows:

## IN THE SPECIFICATION

Please replace the paragraph at page 43, lines 10-13 with the following:

Each of Figs. 10 (a) - 10 (d) is an illustration showing a form of a filter for obtaining a signal variance between neighboring pixels in an example of an embodiment of the invention.

The above amendment is to correct an obvious typographical error.

It is respectfully requested that prosecution on the merits now proceed.

Respectfully submitted,

Leonard Holtz/Esq.

Reg. No. 22/974

Frishauf, Holtz, Goodman, Langer & Chick, P.C.

767 Third Avenue - 25th Floor

New York, New York 10017-2023

Tel. No. (212) 319-4900 Fax No. (212) 319-5101

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Each of Figs. 7 (a) - 7 (d) is an illustration showing how a form is specified in an embodiment of the invention.

Fig. 8 is an illustration showing how a form is specified in an example of an embodiment of the invention.

Fig. 9 is a functional block diagram showing the structure of an image processing apparatus and a flow (processing flow) of image processing in an example of an embodiment for edge feature amount extracting in the invention.

Each of Figs. 10 (a) - 10 (c) is an illustration showing a form of a filter for obtaining a signal variance between neighboring pixels in an example of an embodiment of the invention.

Each of Figs. 11 (a) - 11 (c) is an illustration showing the state of edge distribution in an example of an embodiment of the invention.

Each of Figs. 12 (a) and 12 (b) is an illustration showing the state of edge distribution in an example of an embodiment of the invention.

Fig. 13 is a functional block diagram showing the structure of an image processing apparatus and a flow (processing flow) of image processing in an example of an embodiment for feature amount right and wrong judgment in the invention.